This Page Is Inserted by IFW Operations and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents will not correct images, please do not report the images to the Image Problem Mailbox.

Docket No: 030928/2935P

RECORDATION FORM COVER SHEET PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE U.S. Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

2. Name and address of receiving party(ies):
Name: LSI Logic Corporation Street Address: 1621 Barber Lane City: Milpitas State: CA Zip: 95035
Additional name(s) and address(es) attached? ☐ Yes ■ No.
n dates: (1) November 17, 2003 (3) Same (2) Same
the execution date of the application is: November 17, 2003, Same, B. Patent No.(s)
6. Total number of applications and patents involved: One
7. Total fee (37 CFR 3.41)
nation is true and correct and any attached copy is a true copy of the November 21, 2003

Total number of pages including cover sheet, attachments, and documents: 2

Xudong XU

Docket No: 630928/2935P

<u>ASSIGNMENT</u>

For good and valuable consideration, the receipt of which is hereby acknowledged, I/we, the undersigned, Lav IVANOVIC, Nicholas EIB, and Xudong XU, do hereby sell, assign, and transfer to: LSI Logic Corporation, a corporation existing under the laws of the state of Delaware, having a principal place of business at 1621 Barber Lane, Milpitas, CA 95035 ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for the United States Letters Patent,

United States Letters Patent,
which has been executed by the undersigned concurrently herewith,
which was filed on and assigned Serial No:
and is entitled:
REFLECTIVITY OPTIMIZATION FOR MULTILAYER STACKS
and in and to said application and all divisional, continuing, substitute, renewal, reissue, and all other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and
Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers, communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns or representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and
Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.
Name: Law Ivanovic Name: Micholas KIB Date: 11/17/03
Name: Eli Zudus Date: 11/17/03

Attorney Docket: 030928/2935P

UNITED STATES PATENT APPLICATION

FOR

REFLECTIVITY OPTIMIZATION FOR MULTILAYER STACKS

Inventor(s):

Lav IVANOVIC Nicholas EIB Xudong XU

> Sawyer Law Group LLP 2465 E. Bayshore Road Suite 406 Palo Alto, CA 94303